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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NAKAJYO, Shinsuke et al.

Serial No.: 09/686,958

Filed: October 12, 2000

Group Art Unit: 2824

Examiner: Adam Pyonin

P.T.O. Confirmation No.: 5708

For. **MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE**

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Date: August 29, 2002

Sir:

In response to the Office Action dated June 4, 2002, please amend the above-identified application as set forth below:

IN THE SPECIFICATION:

Amend the specification as follows:

(Page 1, lines 2-3): MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR

a' DEVICE WITH QUALITY MARKINGS ON THE SEALING RESIN

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